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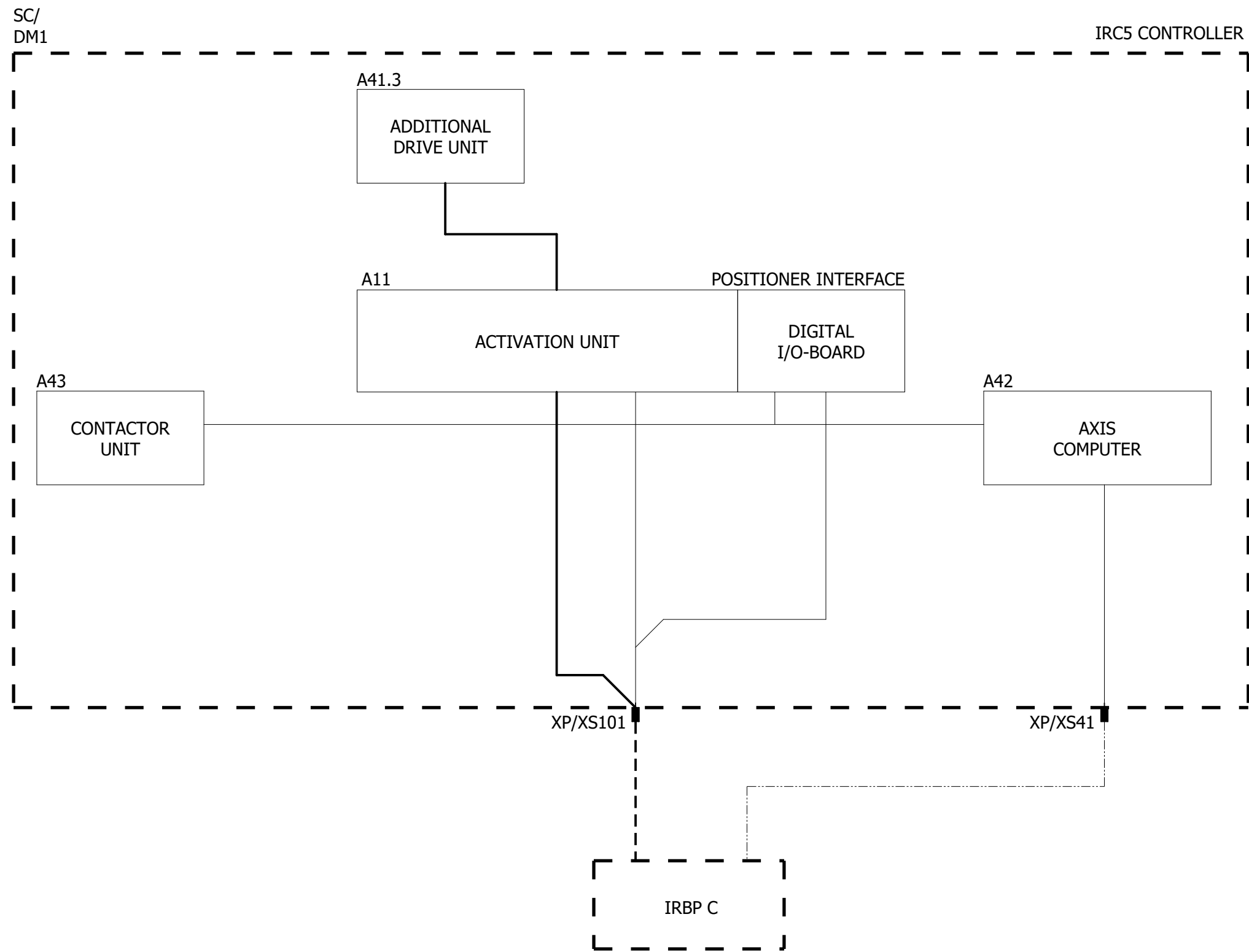
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ECO 130690 WI 499329 Page 8.b/2: Varistor "X15" adjusted & JMP added.	Prepared by, date: EM, 2023-12-20	Approved by, date: SH, 2020-12-21		COPYRIGHT PAGE	Document no. 3HAC035754-001	Rev. Ind 04
						Page 1 Next 2 Total 15

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----- CONNECTION NOT INCLUDED IN CIRCUIT DIAGRAM.

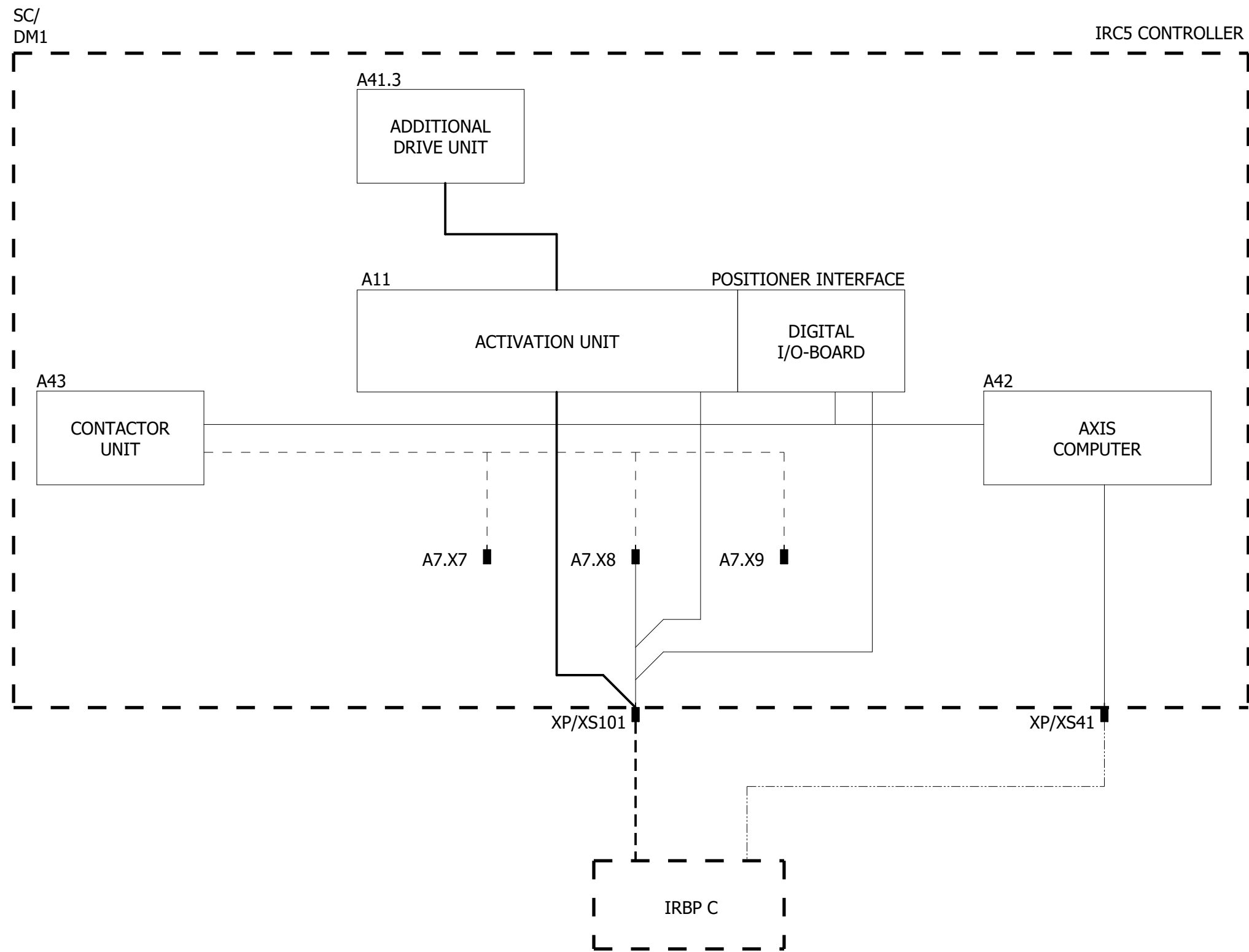
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SERVICE DIAGRAM POS IF C.D 2009
 BLOCK DIAGRAM Before Release 14.1
 IRBP C

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Document no. 3HAC035754-001	Rev. Ind 04
	Page 4.a Next 4.b Total 15

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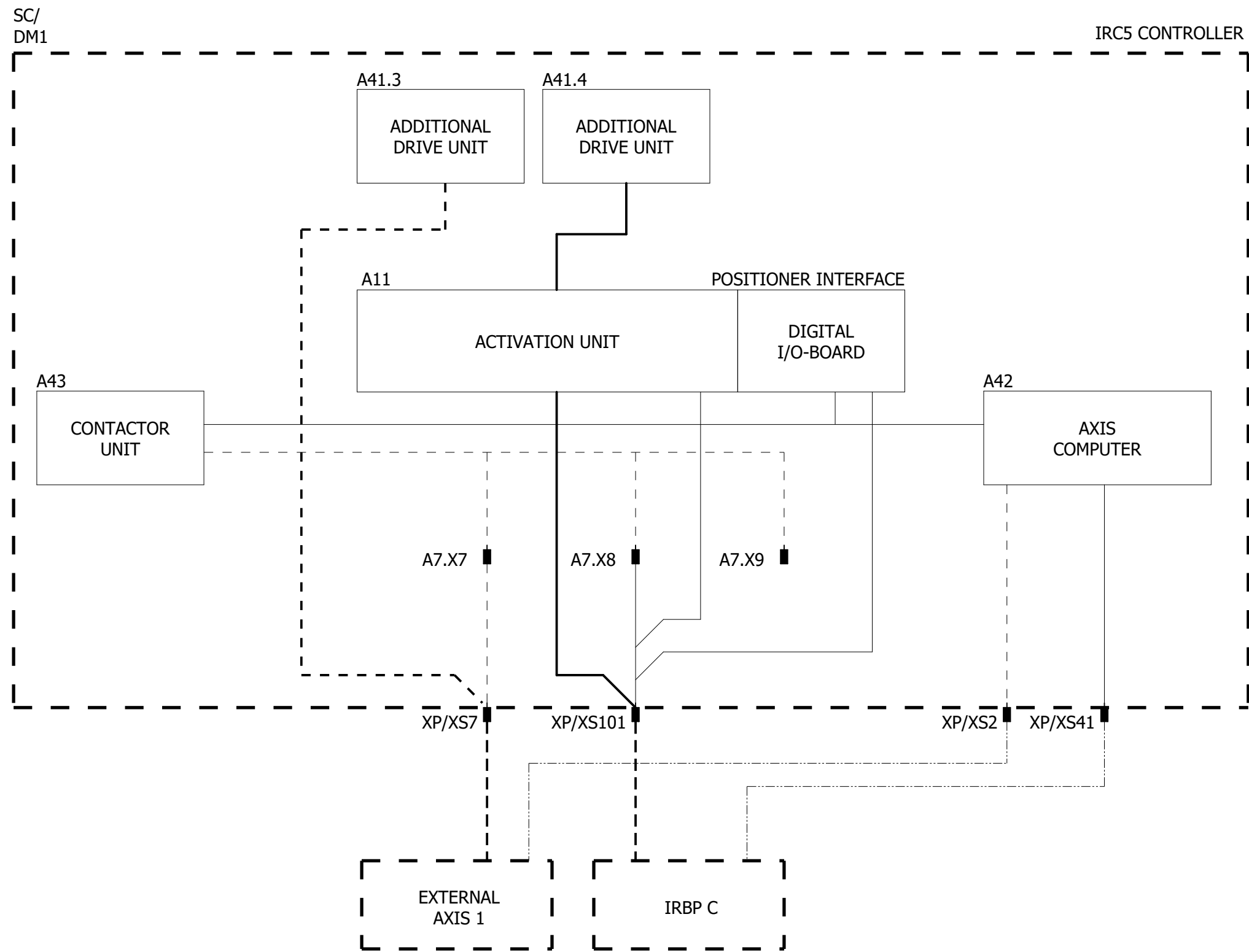
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SERVICE DIAGRAM POS IF C.D 2009
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 SCENARIO A: IRBP C

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Document no. **3HAC035754-001** | Rev. Ind **04** | Page 4.b
 Next 4.c
 Total 15



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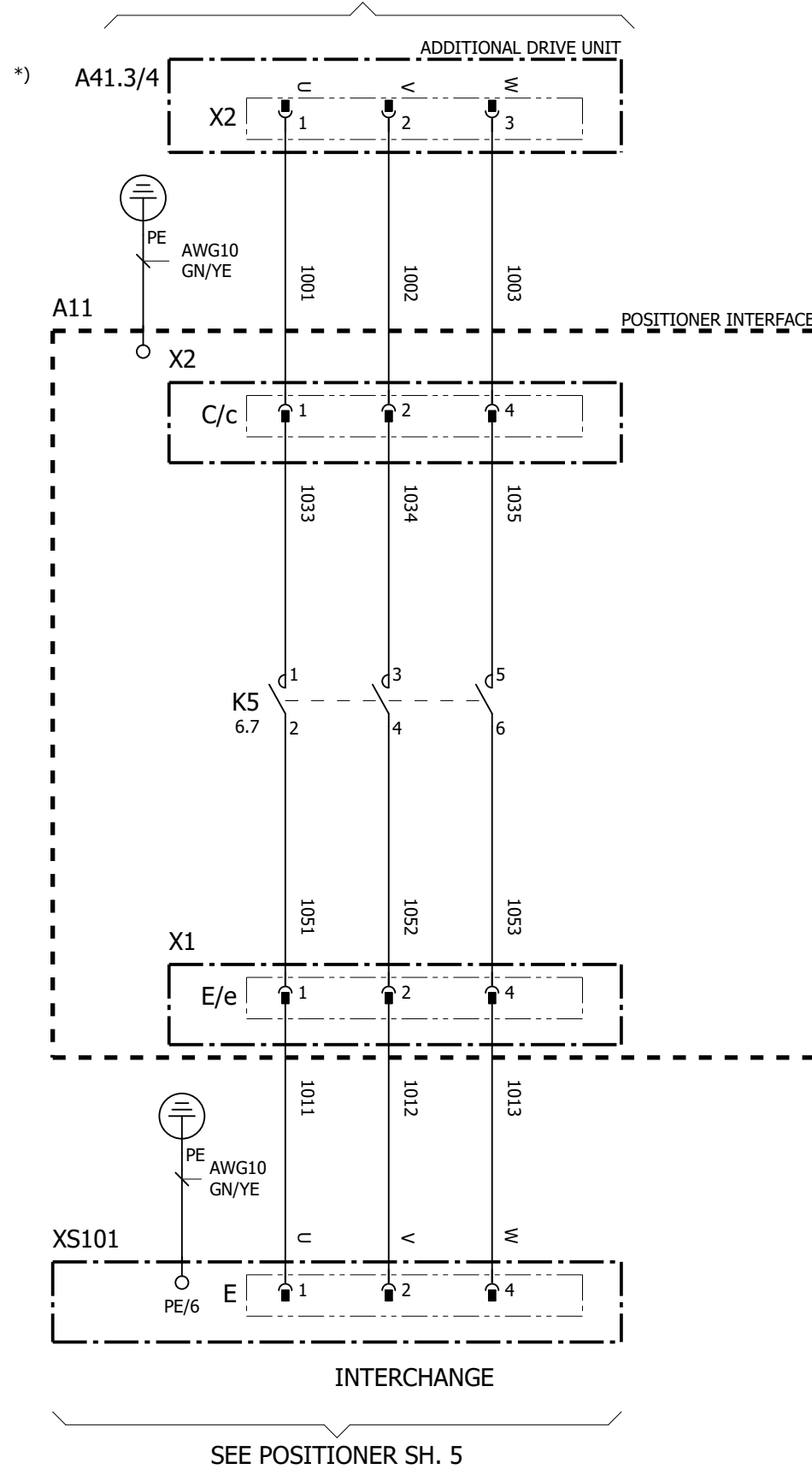


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 SCENARIO B: 1 EXT.AXIS AND IRBP C

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Document no. 3HAC035754-001	Rev. Ind 04
	Page 4.c Next 5 Total 15

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SEE EL.DWGS FOR THE IRC5 CONTROLLER



*) SCENARIO A: CONNECTED TO A41.3.X2
SCENARIO B: CONNECTED TO A41.4.X2

CONNECTED WITH AWG12 BK,
IF NO OTHER NOTATION

SEE POSITIONER SH. 5

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SERVICE DIAGRAM POS IF C.D 2009
MOTOR INTERCHANGE

Status:
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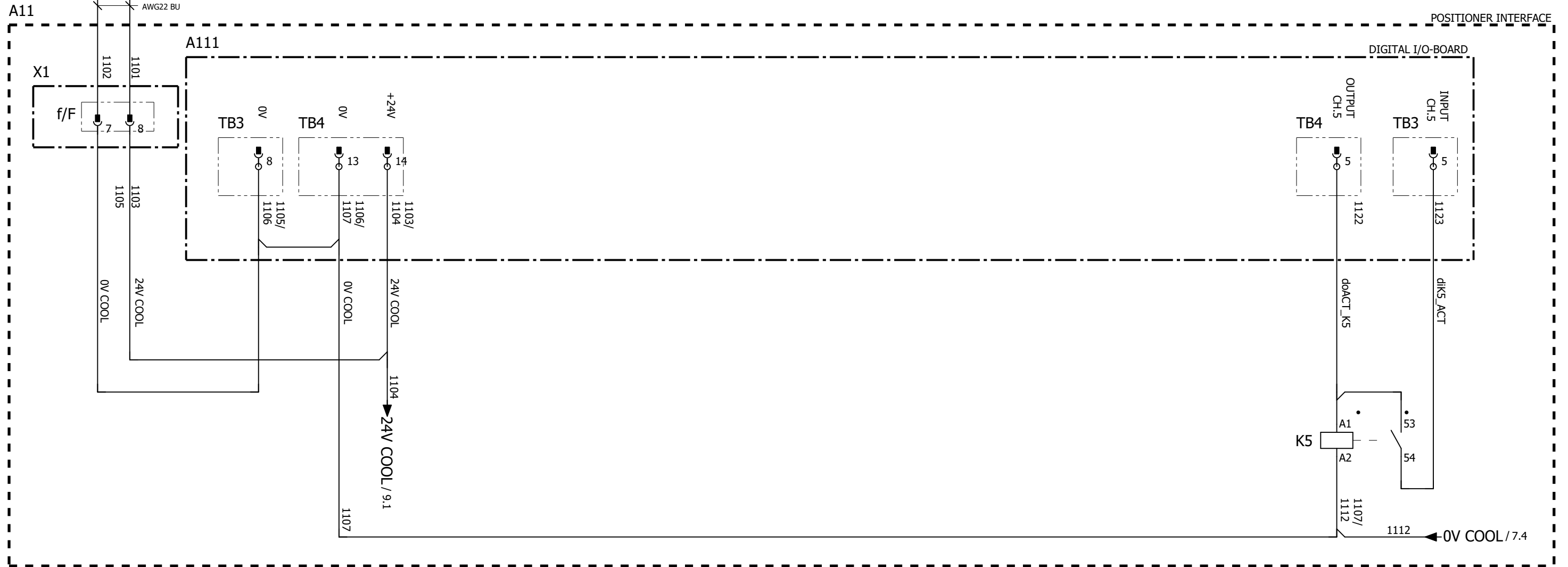
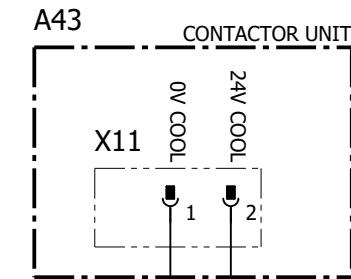
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Rev. Ind Page 5
04 Next 6
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INTERCHANGE

- 1 2 5.4
- 3 4 5.5
- 5 6 5.5
- 13 14 8.a.2
- 13 14 8.b.2
- 53 54 6.8
- 62 61 9.8
- 72 71 9.8

CONNECTED WITH AWG18 BU,
IF NO OTHER NOTATION

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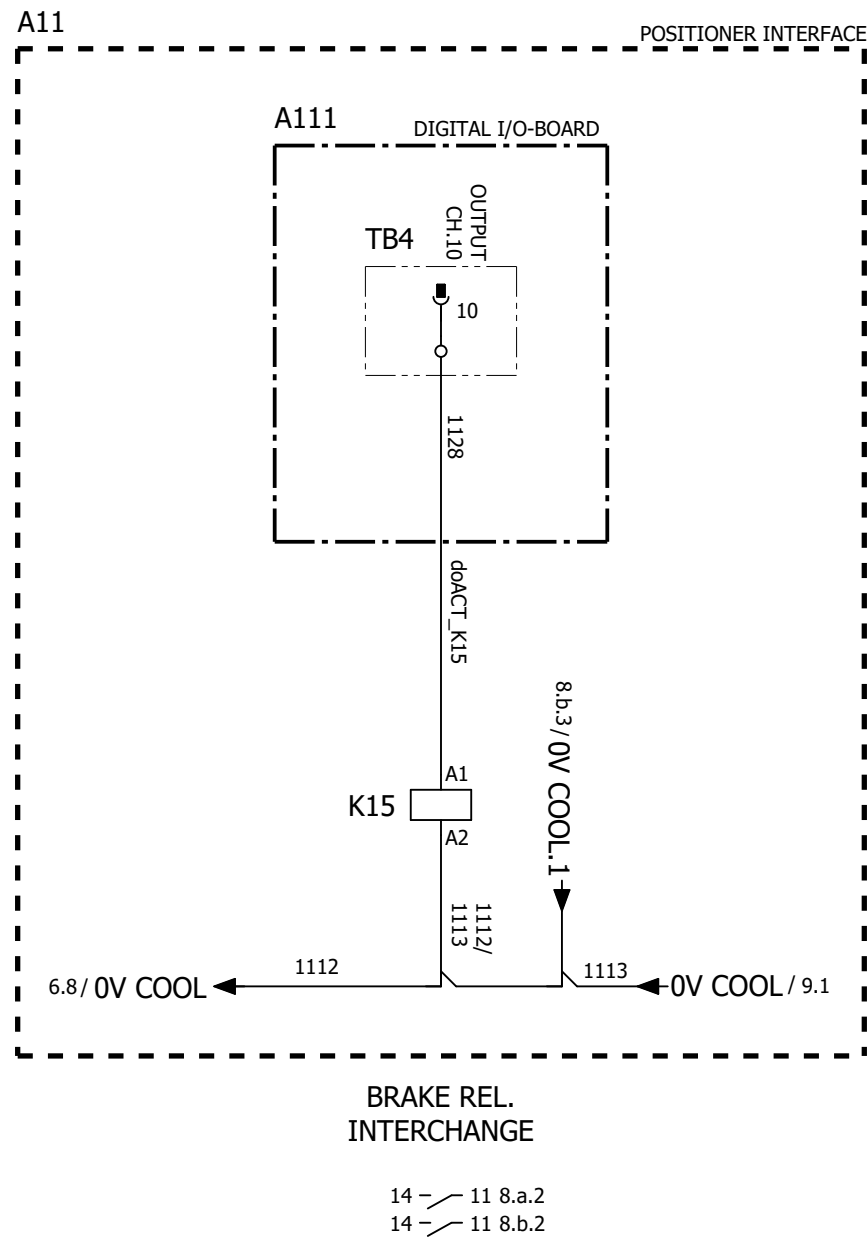
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Document no. 3HAC035754-001
Rev. Ind 04
Page 6
Next 7
Total 15

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BRAKE RELEASE

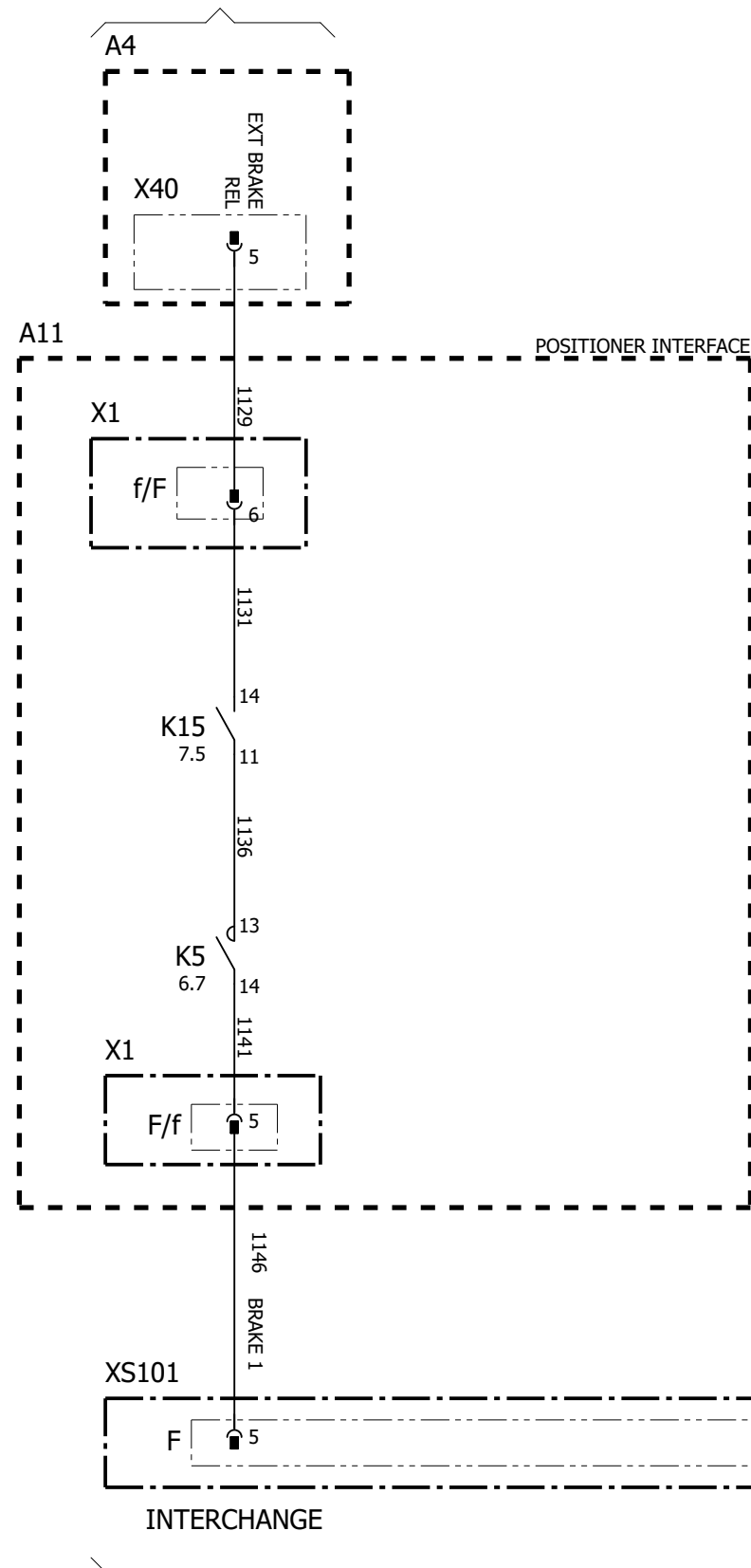
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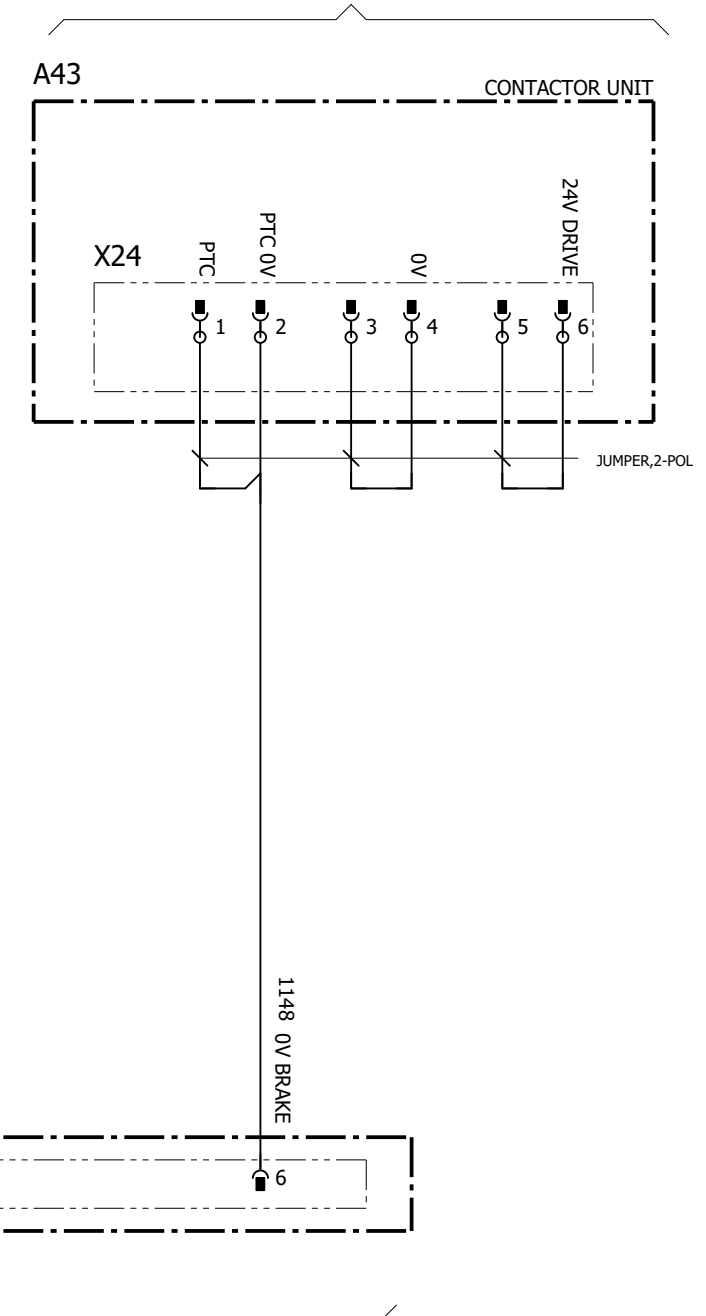
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Rev. Ind Page 7
04 Next 8.a
Total 15

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SEE POSITIONER SH. 5

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MOTOR SIGNALS
BEFORE RELEASE 14.1

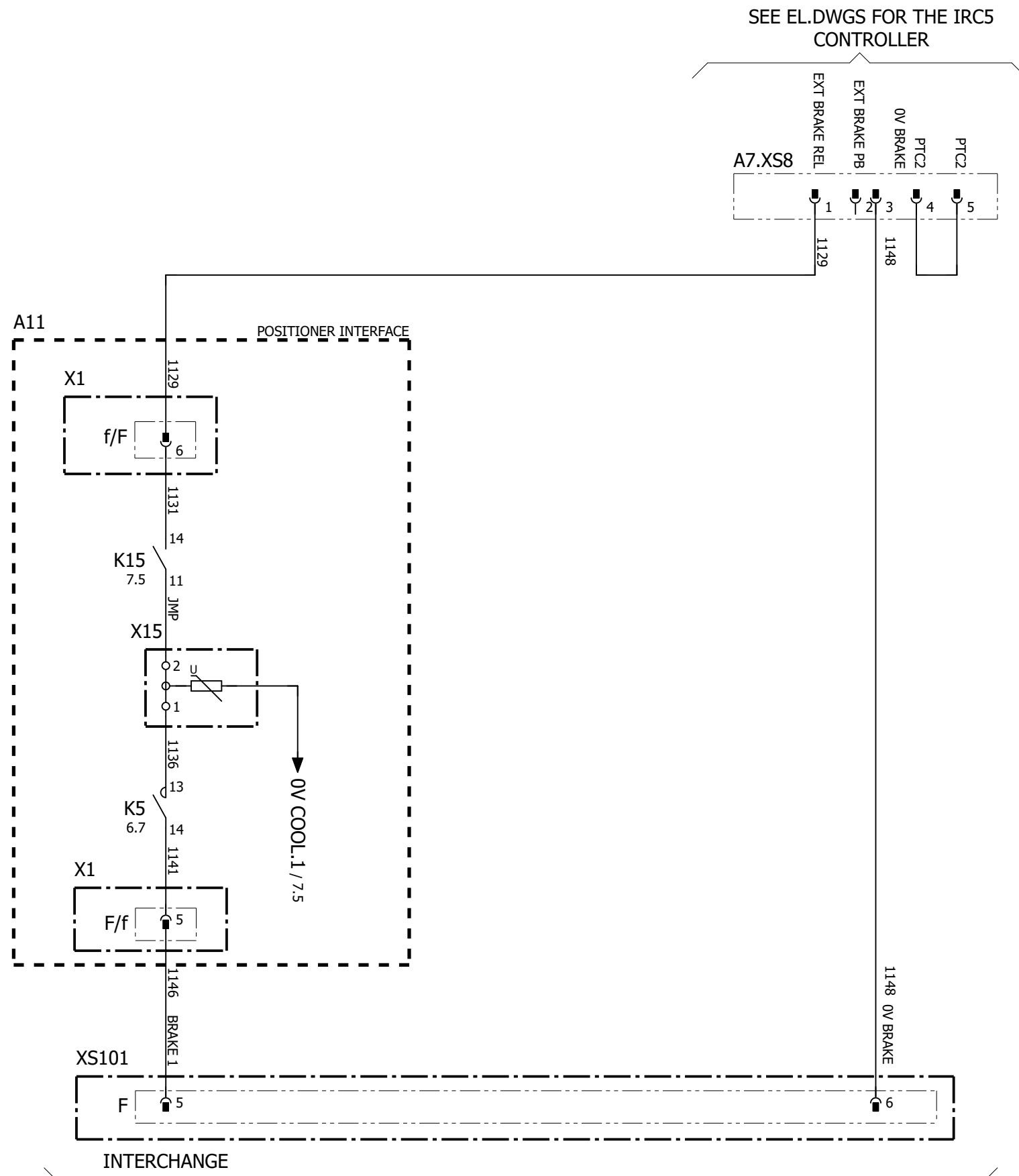
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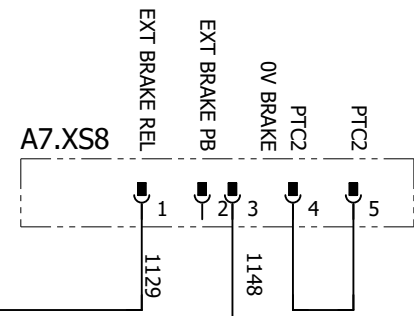
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Rev. Ind
04
Page 8.a
Next 8.b
Total 15

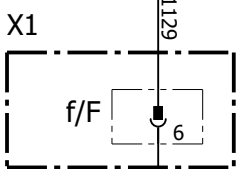
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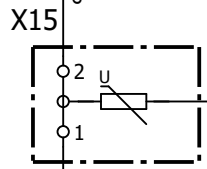
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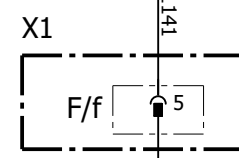
A11 POSITIONER INTERFACE



K15 7.5



K5 6.7



XS101



INTERCHANGE

SEE POSITIONER SH. 5

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 MOTOR SIGNALS
 RELEASE 14.1

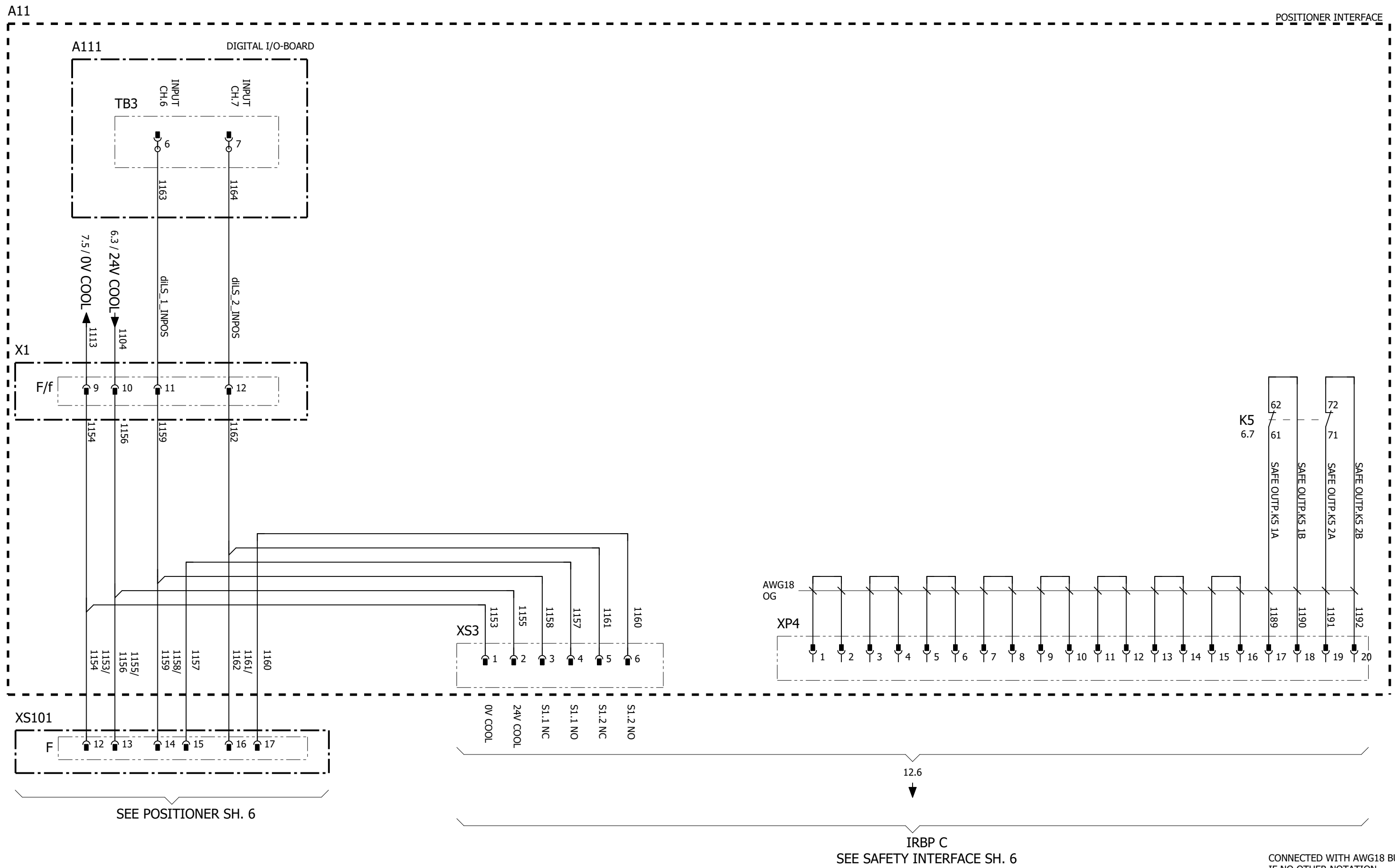
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Rev. Ind Page 8.b
 04 Next 9
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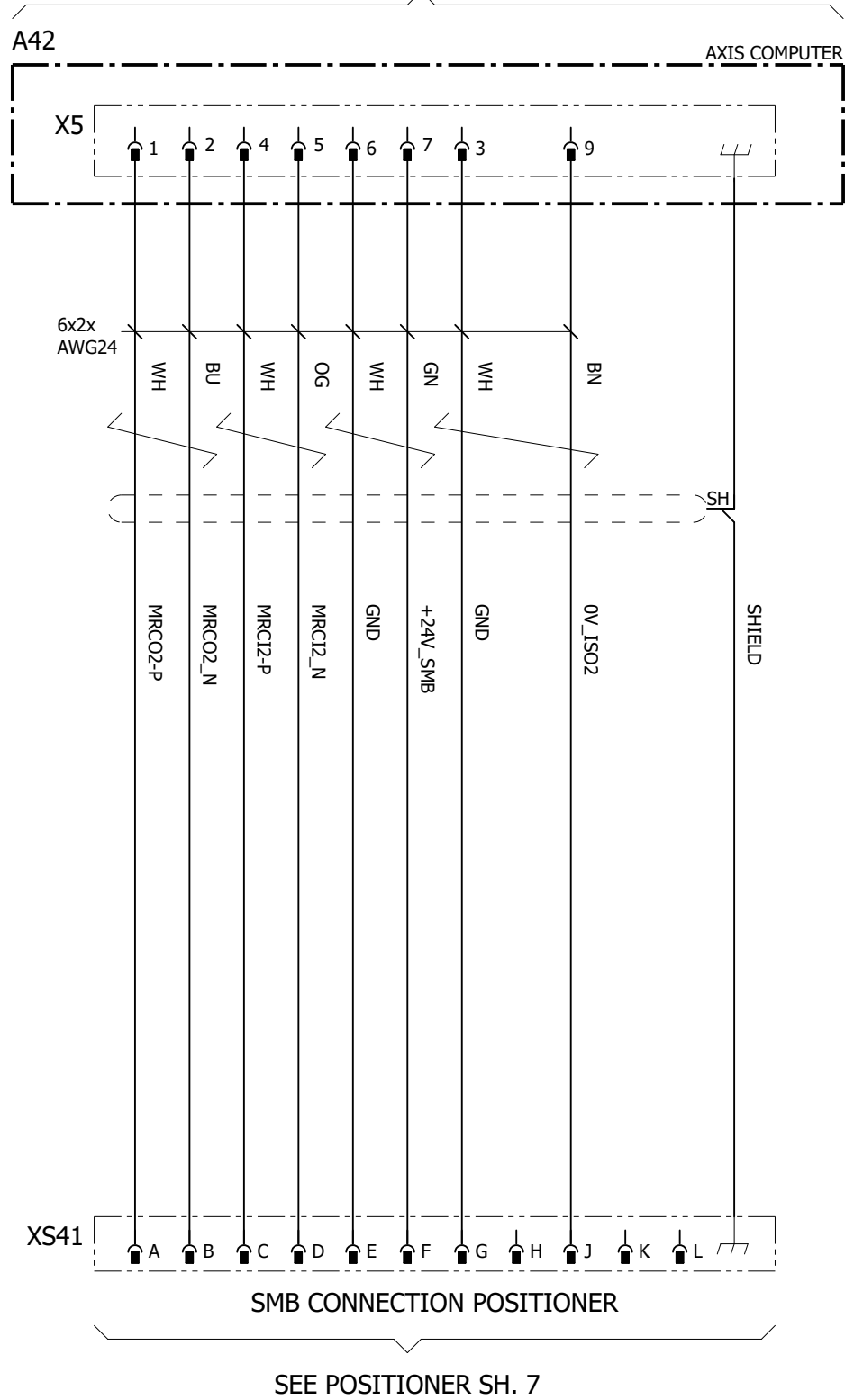
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 Next 10
 Total 15

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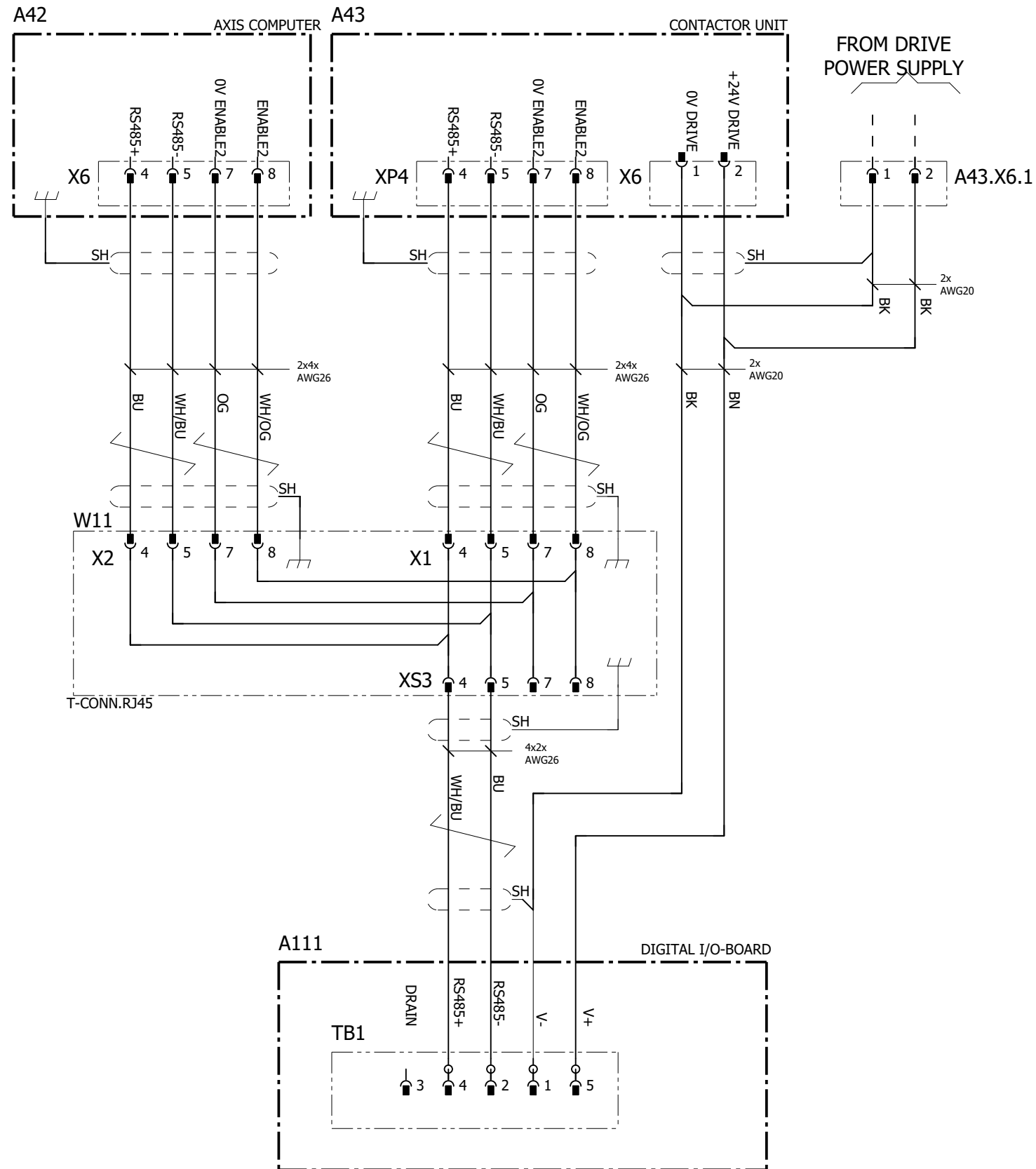


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	Page 10 Next 11 Total 15

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 BUS CONNECTIONS

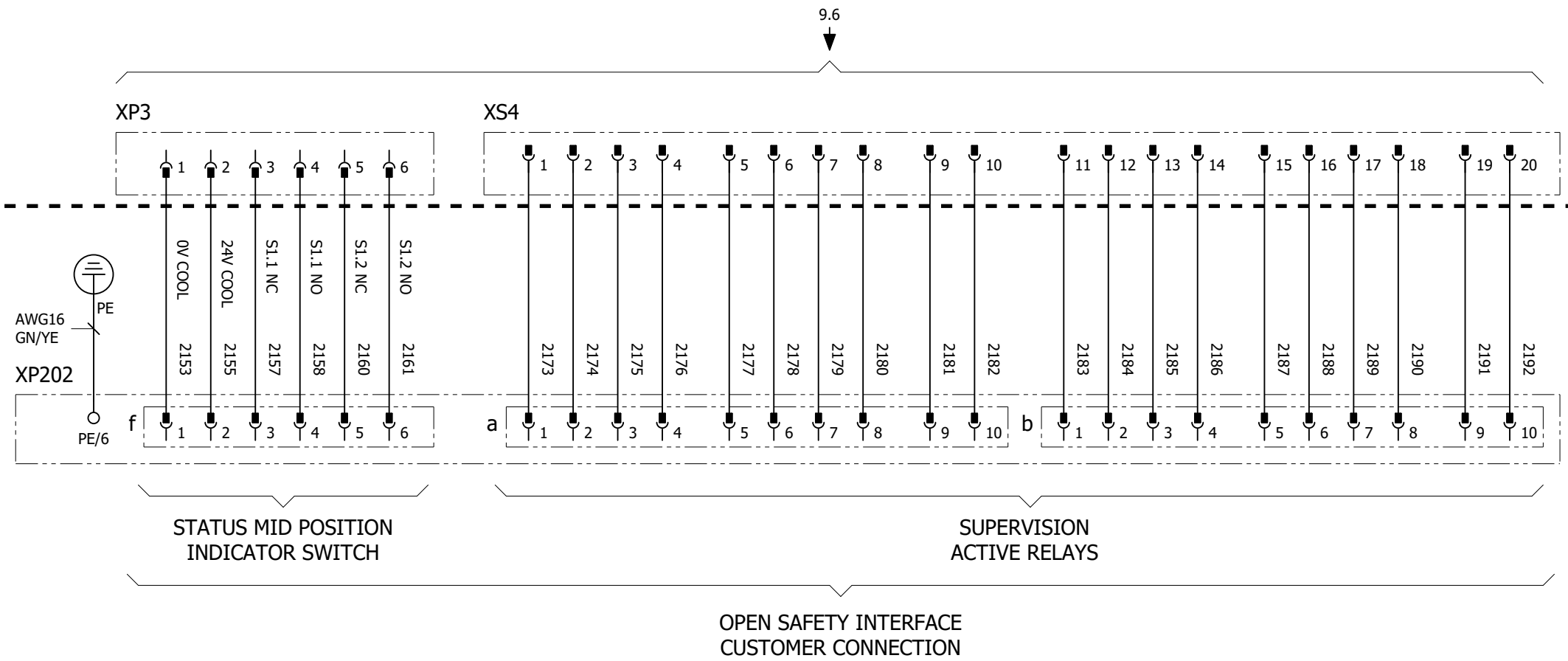
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Document no. **3HAC035754-001** | Rev. Ind **04** | Page 11
 Next 12
 Total 15

A11

POSITIONER INTERFACE



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Rev. Ind Page 12
04 Next
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